

Notic of Ref rences Cited

Application/Control No.

09/103,873

Applicant(s)/Patent Under
Reexamination
NAGANO ET AL.

Examiner

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Art Unit

2815

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U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
	A	US-			
	B	US-			
	C	US-			
	D	US-			
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FOREIGN PATENT DOCUMENTS

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NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Wolf et al., "Silicon Processing for the VLSI Era Volume 1: Process Technology", Lattice Press, 1986, pp. 182-194.
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*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
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